

Fig. 1

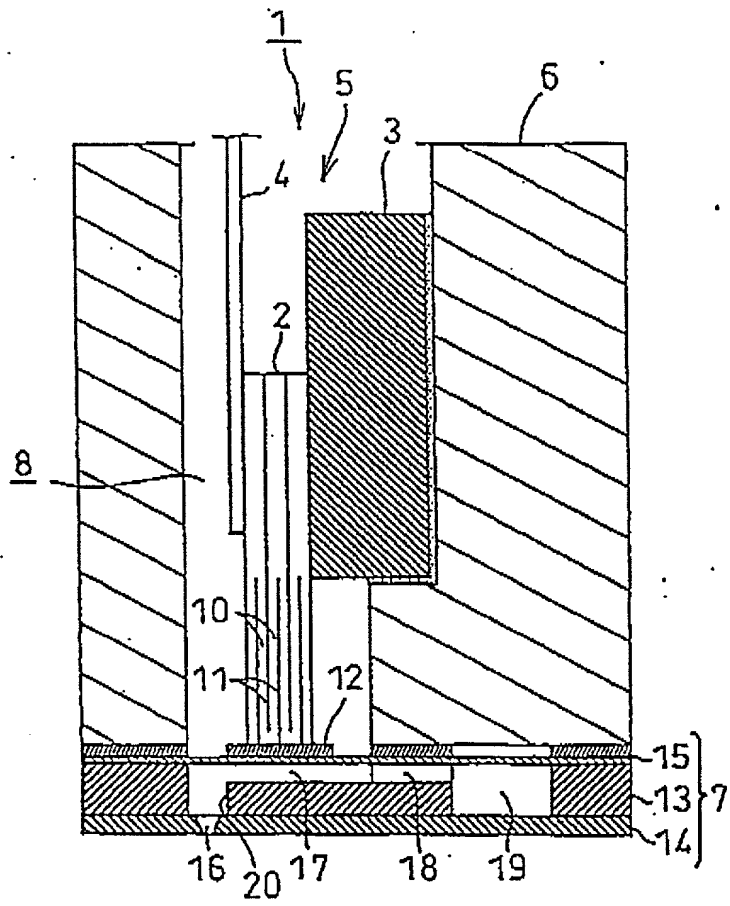


FIG. 1

Fig. 2

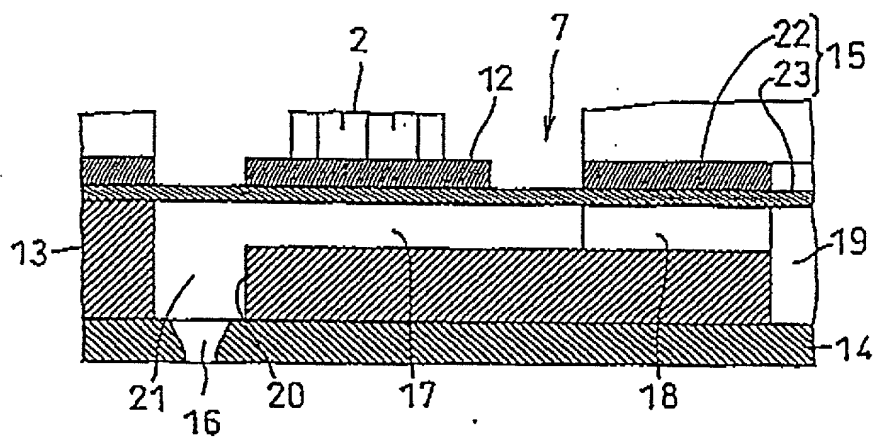


Fig. 3

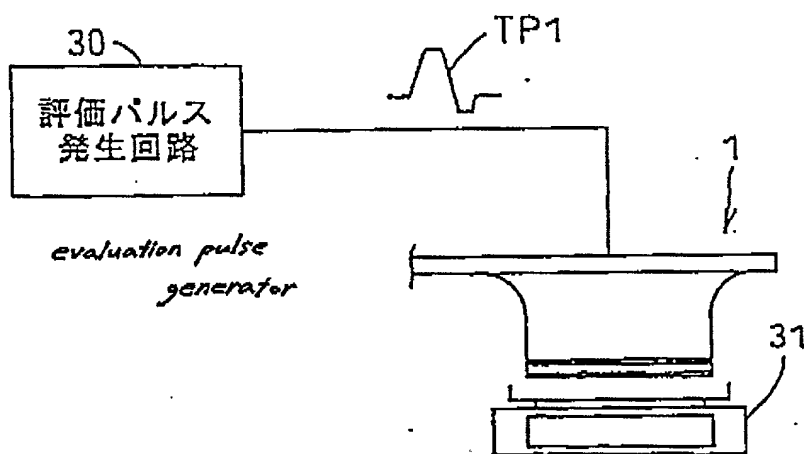


Fig. 4

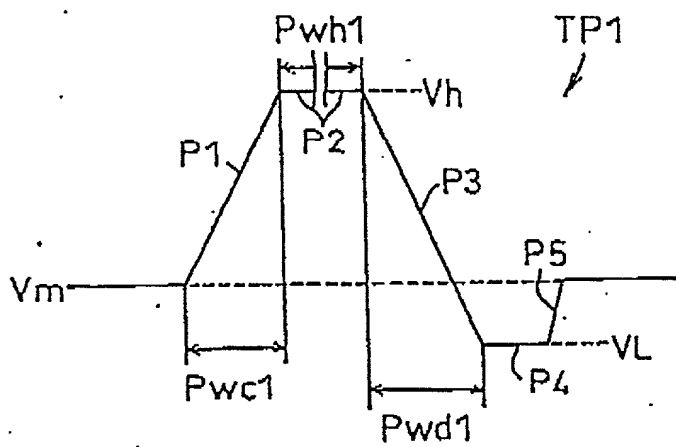


Fig. 5

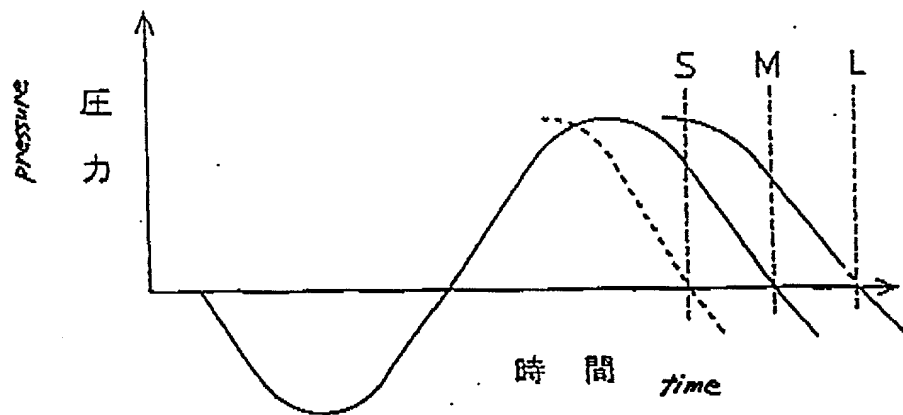
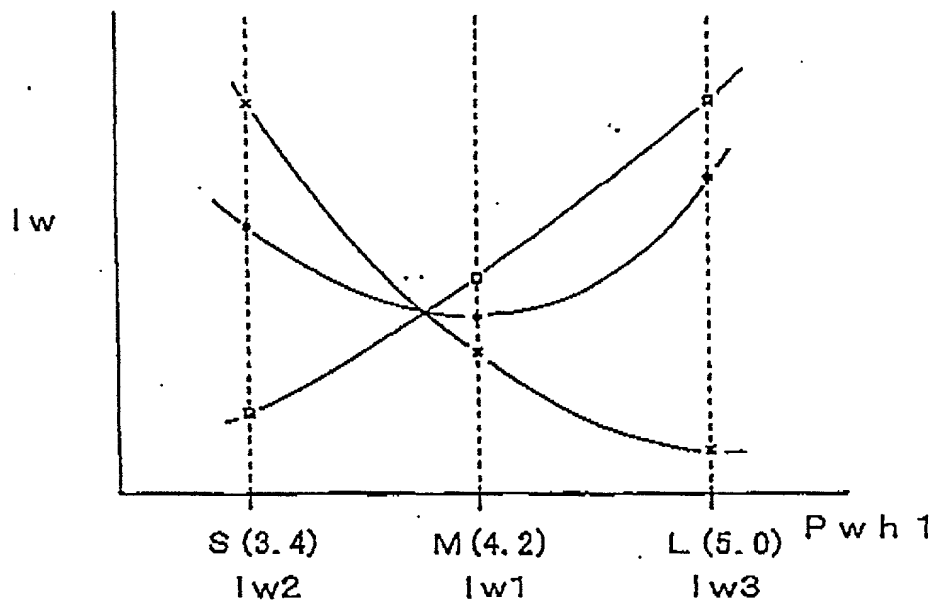


Fig. 6

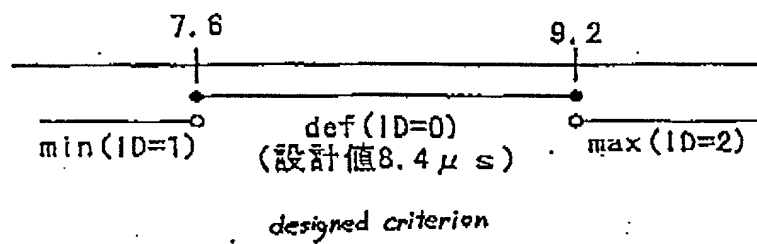


		lw			
Tc ランク ID	0	$lw2 >$	$lw1 <$	$lw3$	
		$lw2 >$	$lw1 =$	$lw3$	
		$lw2 =$	$lw1 <$	$lw3$	
	1	$lw2 <$	$lw1 <$	$lw3$	
	2	$lw2 >$	$lw1 >$	$lw3$	
	エラー	$lw2 =$	$lw1 =$	$lw3$	
		$lw2 =$	$lw1 >$	$lw3$	
		$lw2 <$	$lw1 =$	$lw3$	

Fig. 7

Tc rank ID error

Fig. 8



0942764 09340
T07E80 4924650

Fig. 9

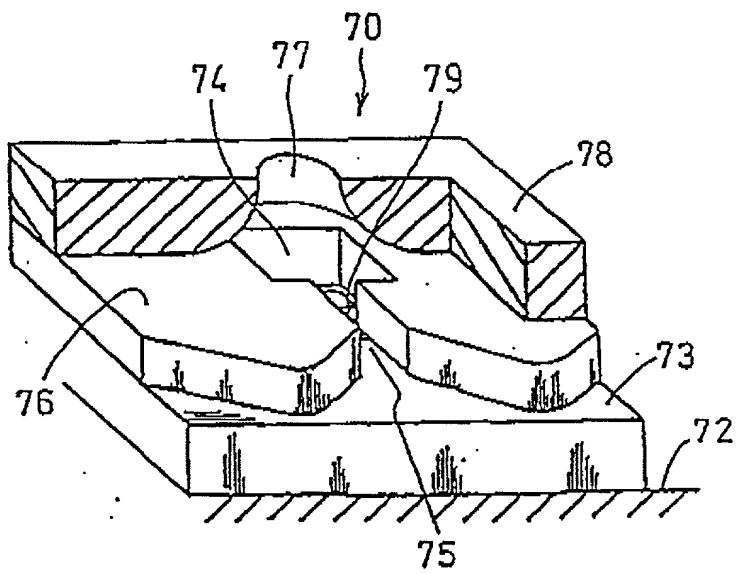


Fig. 10

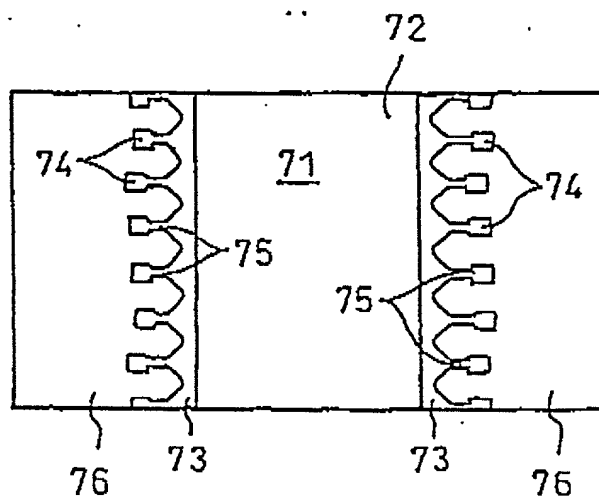


Fig. 11

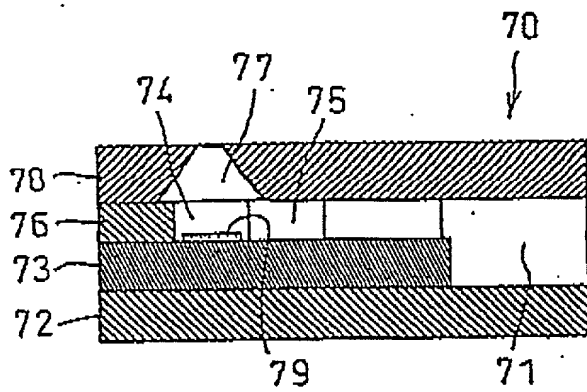


Fig. 12A

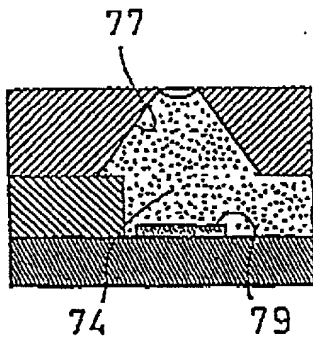


Fig. 12B

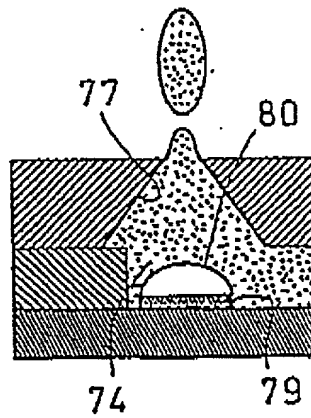
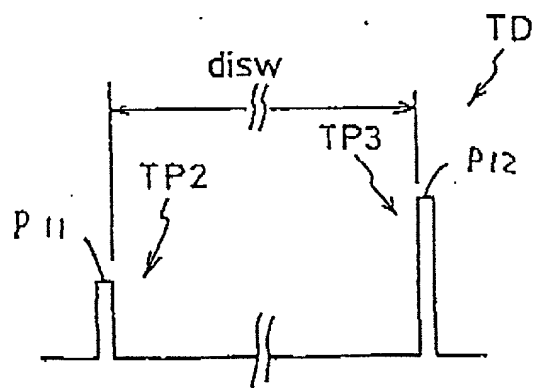


Fig. 13



09943764 083101
TOP SECRET

Fig. 14

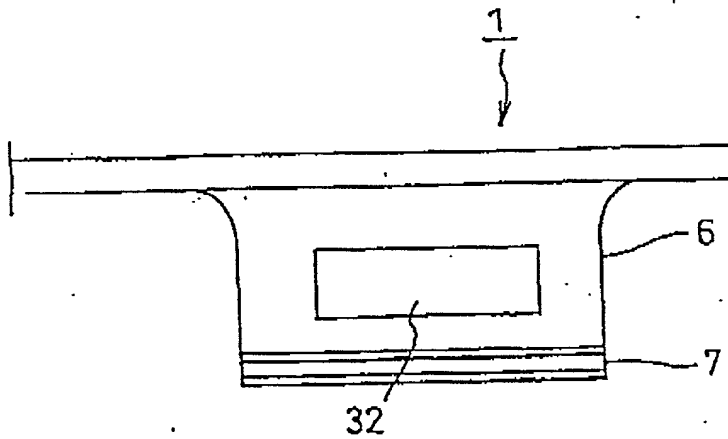
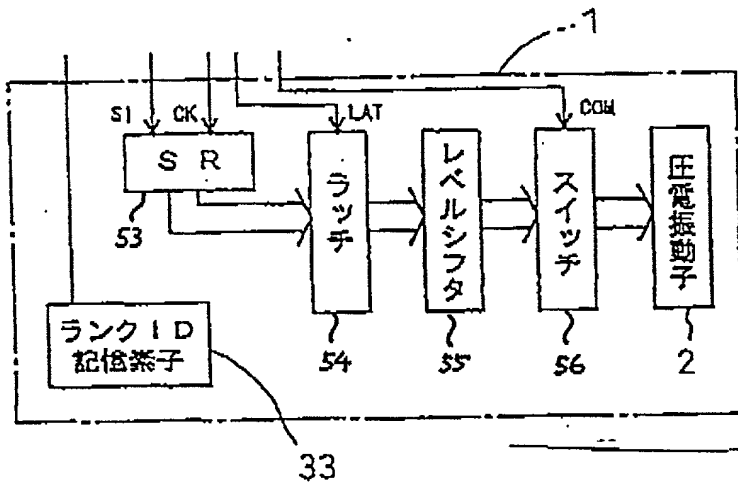
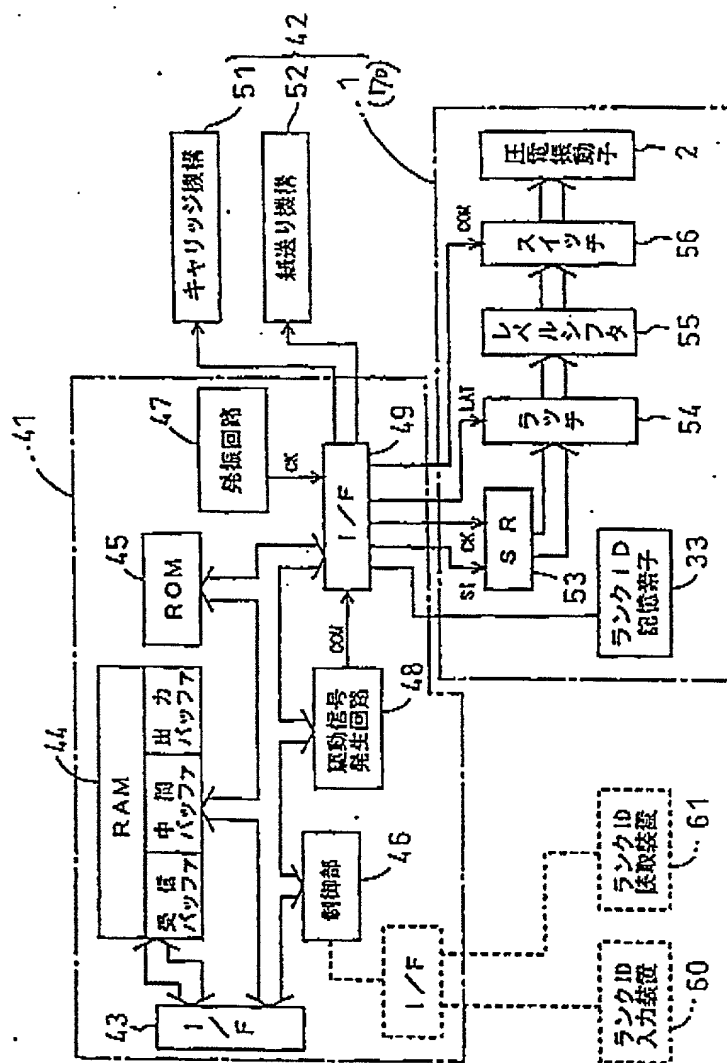


Fig. 15



- 2: piezoelectric vibrator
- 33: rank memory
- 53: shift register
- 54: latch
- 55: level shifter
- 56: switcher

Fig. 16



- 2: piezoelectric vibrator
- 33: rank ID memory
- 44: reception buffer
- 45: intermediate buffer
- 47: output buffer
- 46: controller
- 47: oscillator
- 48: drive signal generator
- 51: carriage mechanism
- 52: paper feeding mechanism
- 53: shift register
- 54: latch
- 55: level shifter
- 56: switch
- 60: rank ID input device
- 61: rank ID reader

Fig. 17

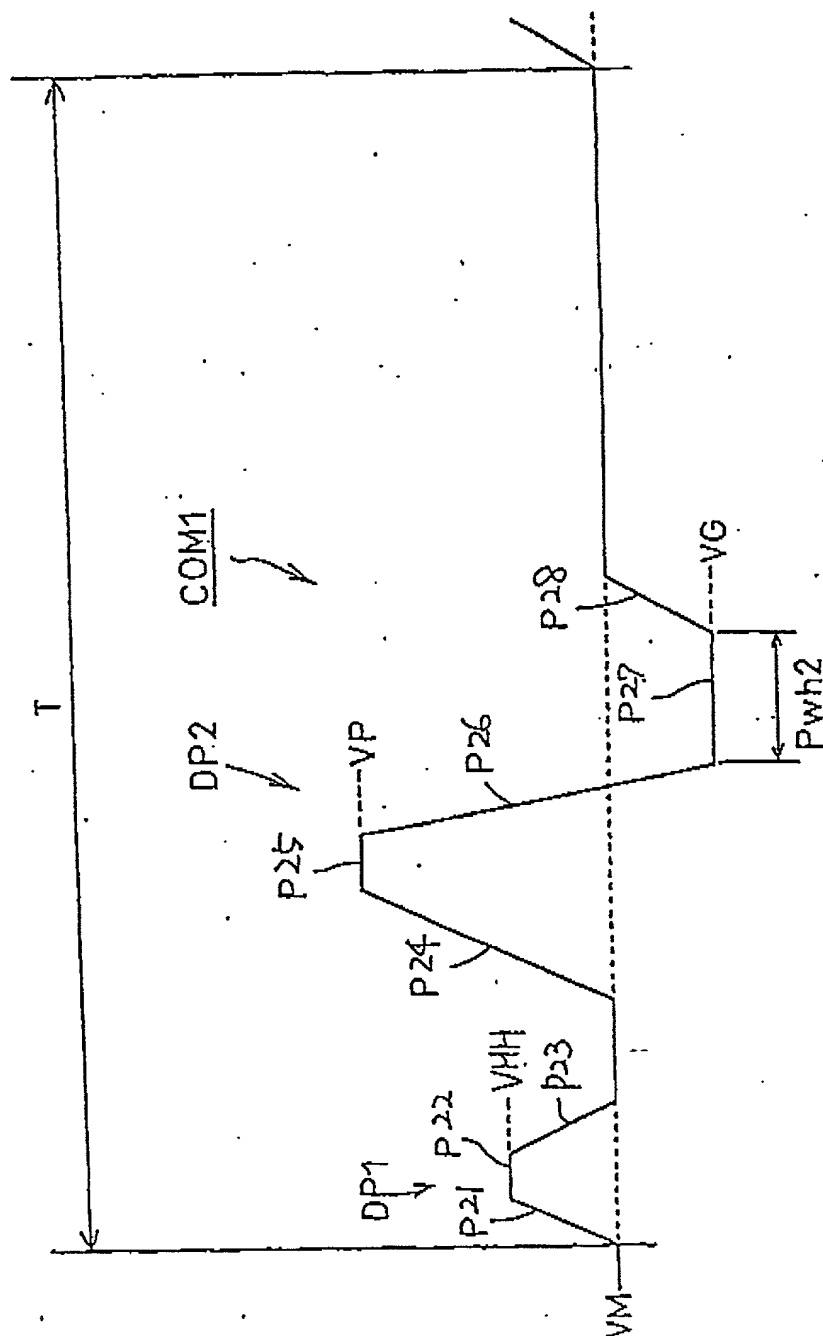
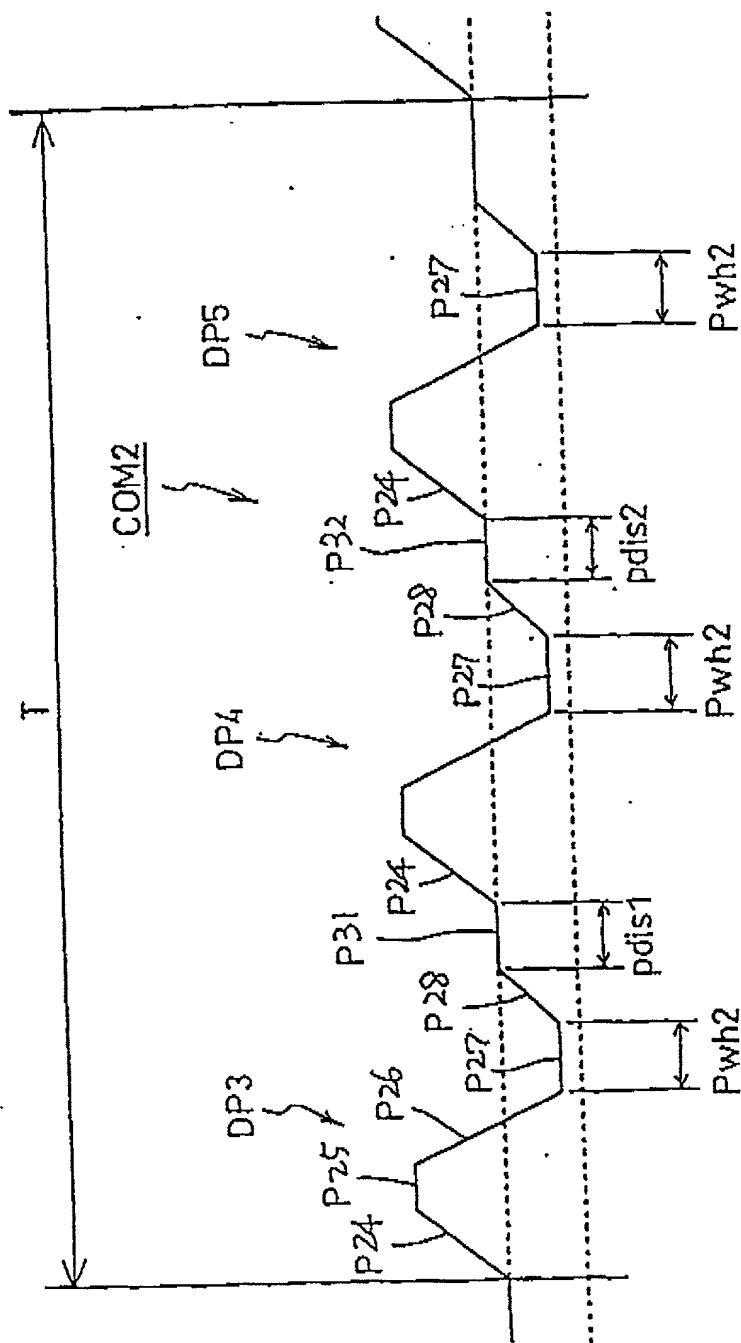


Fig. 18



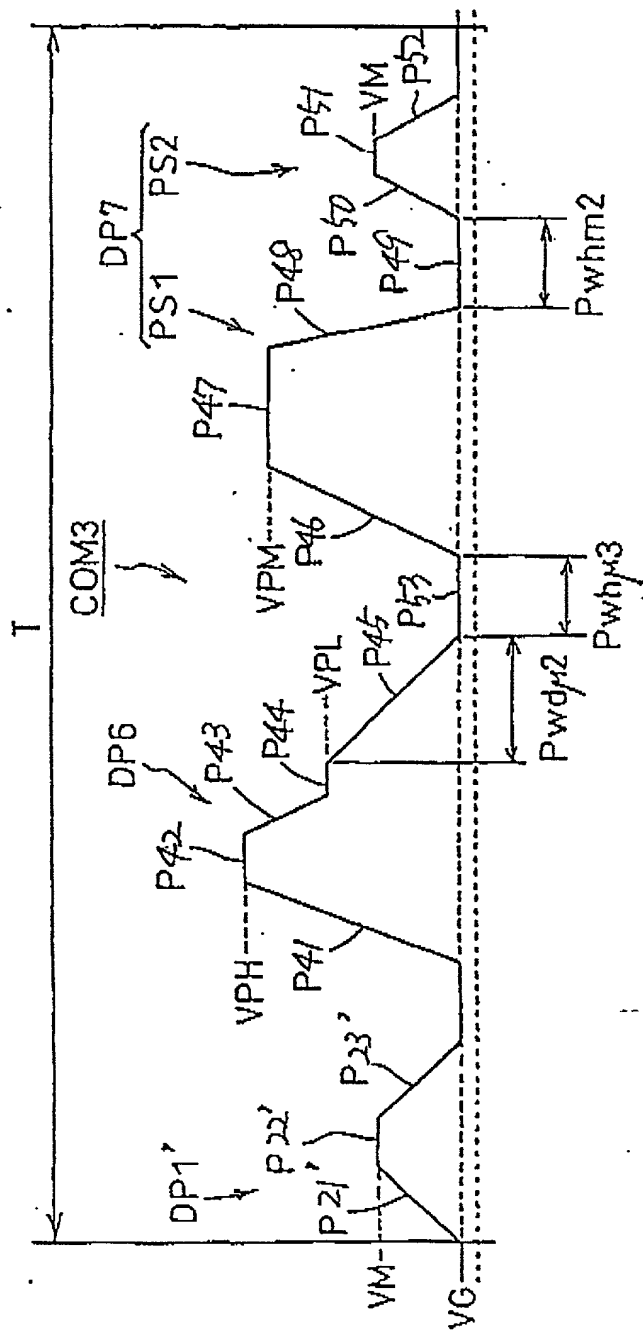


Fig. 20

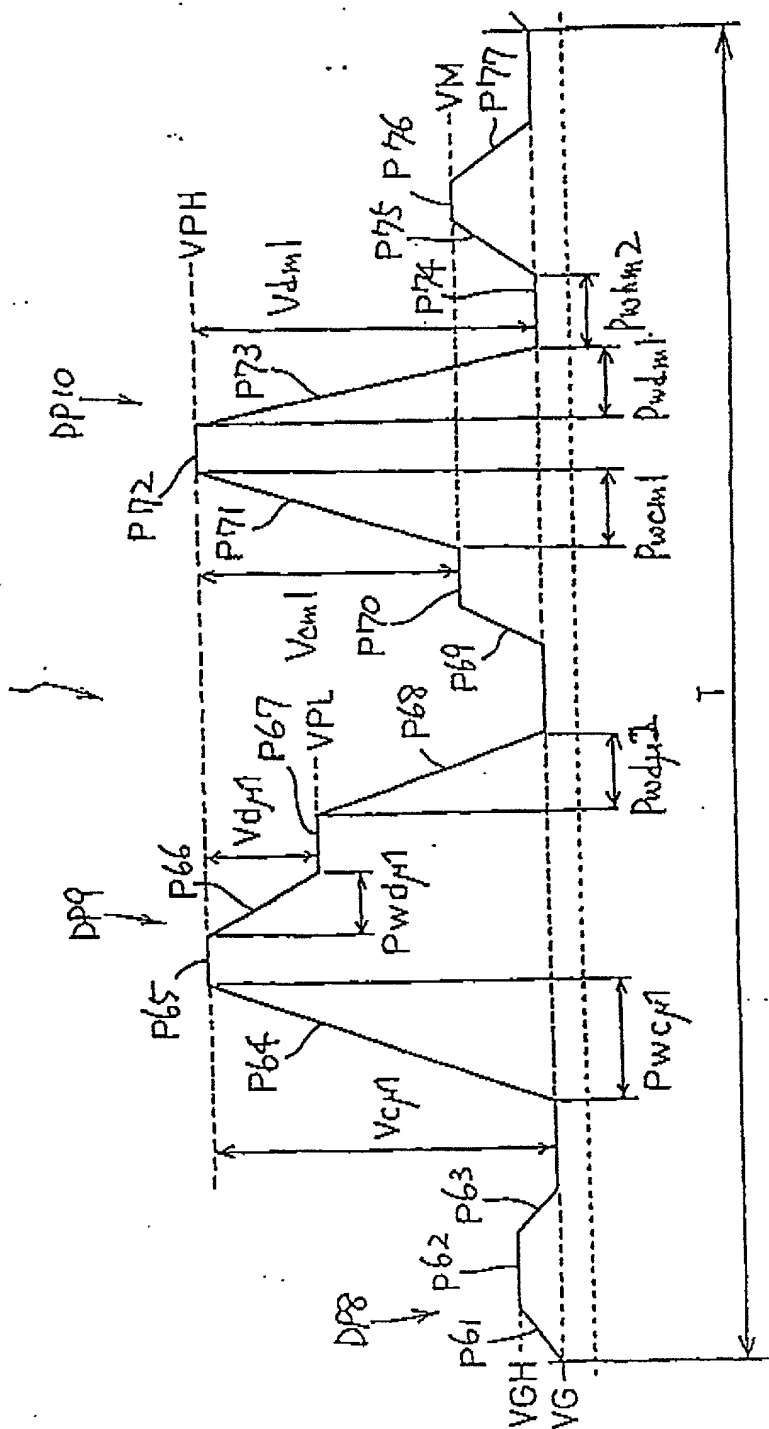
com4

Fig. 21.

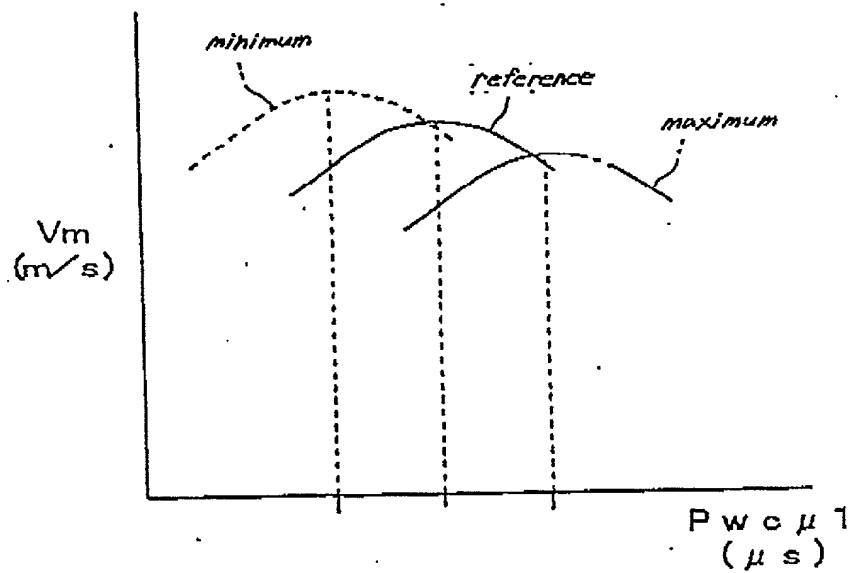


Fig. 22

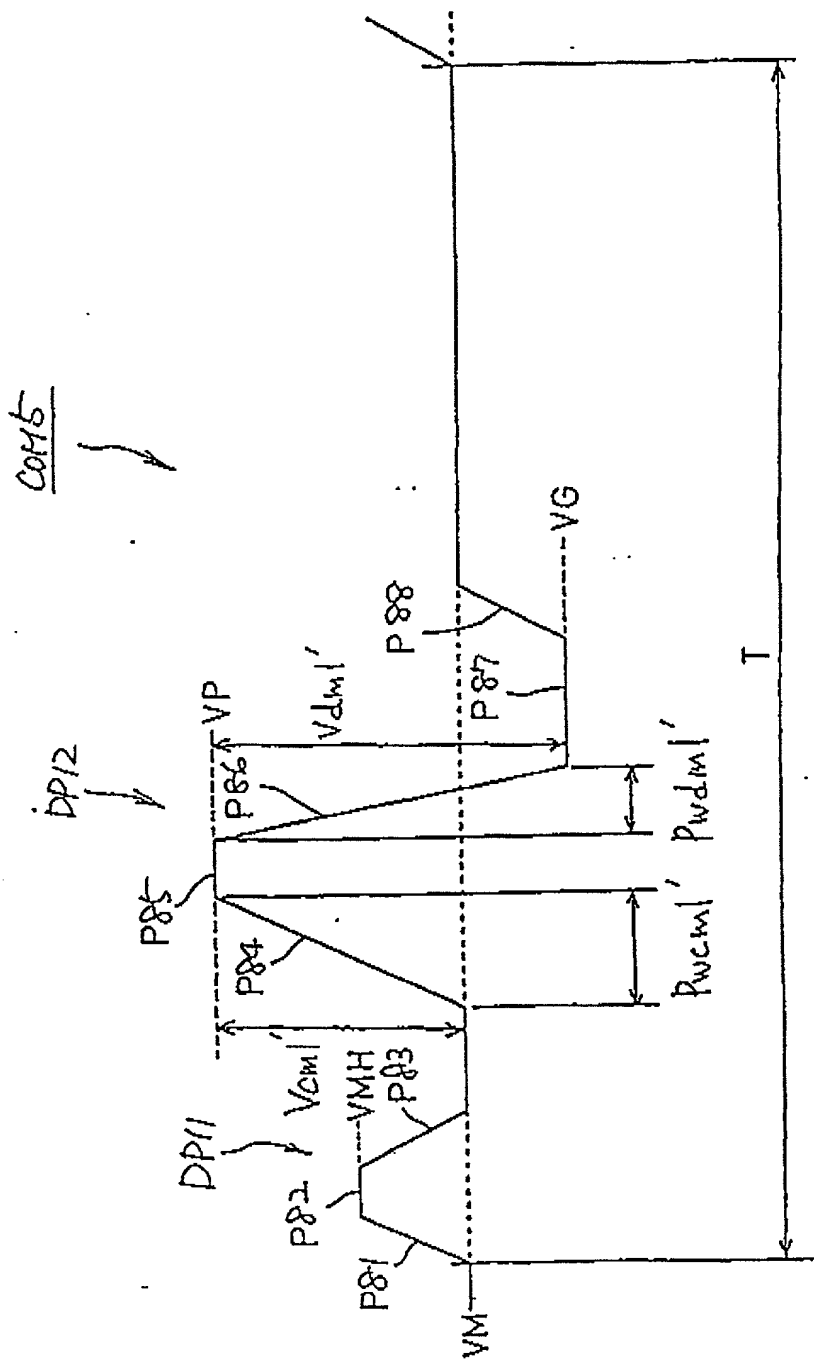


Fig. 23

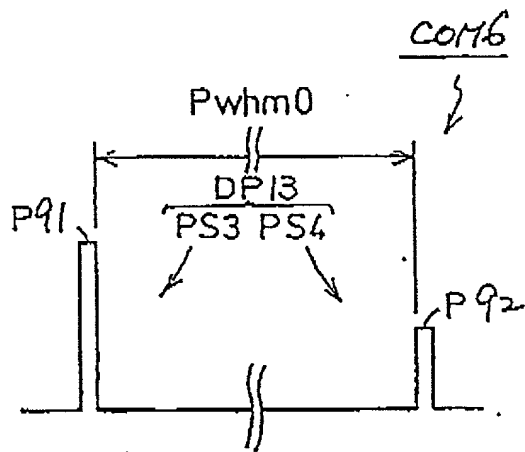


Fig. 24

